



राष्ट्रीय प्रौद्योगिकी संस्थान दिल्ली

NATIONAL INSTITUTE OF TECHNOLOGY DELHI

(शिक्षा मंत्रालय, भारत सरकार के अधीन एक स्वायत्त संस्थान)

(An autonomous Institute under the aegis of Ministry of Education, Govt. of India)

Plot No. FA7, Zone P1, GT Karnal Road, Delhi-110036, INDIA

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वेबसाइट/Website: www.nitdelhi.ac.in

Special Advertisement for Admission to PhD (Full-Time and Part-Time) under the “Visvesvaraya PhD Scheme for Electronics and IT”
Programme in Engineering for the Autumn Semester, Academic Year: 2026-27
Advt. No.: NITD/R&C/Visvesvaraya/PhD/2026-27

Online Applications are invited for admission to PhD programme (Full-Time and Part-Time) under Visvesvaraya Scheme. Applicants must apply online (via Google Form), as mentioned in the “**Procedure for Online Application/ How to Apply**” Section. The Broad areas of Research, Eligibility Criteria, Selection Procedure, Teaching Assistantship-cum-Scholarship, Visvesvaraya Scheme, etc. are given in the advertisement.

S. No.	Department	Category	Discipline/Area of Research
1.	Electronics Communication & Engineering (ECE)	Full-Time	Refer Table II
2.	Computer Science and Engineering (CSE)/ Electrical Engineering (EE)/ Electronics Communication & Engineering (ECE)	Part-Time	Refer Table I

How to apply: The link of fill the online application form can be accessed through Institute website <https://nitdelhi.ac.in>. Please submit separate application forms, if applying for more than one category. An application fee of Rs. 1000/- (Rs. 500/- for SC/ST/PWD candidates) is required to be paid online. The candidates are required to apply online and **there is no requirement to send the Hard Copy** of the application form to the Institute. Application fee shall not be refunded in any case.

A candidate interested in applying in more than one category shall be required to submit separate application forms along with appropriate fees and documents. At the time of interview no conversion from one category to another will be allowed.

Last date for submission of online application form is **October 8th, 2026**.

For more details, the candidates are advised to visit the Institute website <https://nitdelhi.ac.in> for updates.

Table I (Part-Time)

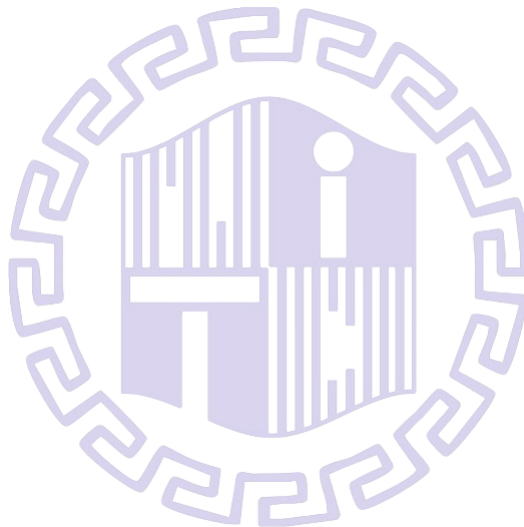
A. Department wise Broad Areas of Research in which applications are invited:

	Department	Broad Areas of Research
1.	Computer Science and Engineering (CSE)	Internet of Things, Databases, Data Warehousing and Data Mining, Data Analytics, Data Science, Big Data, Spatial Data Mining, Geographic Information Systems (GIS), Machine learning, Machine Learning for Healthcare, Computer Vision, Digital Image Processing, Graph Data Mining and Analysis, Big Data Infrastructure and Optimization, Health Informatics, Software Analytics, Mining Software Repositories, Anomaly Detection in Big Data Streams, Generative Artificial Intelligence, Explainable AI (XAI), Deep Learning for Natural Language Processing (NLP), Ubiquitous Computing, Complex Networks, Dynamics on/of networks, Graph Spectra, Time Varying Networks, Community structure, Link Analysis, Recommender Systems, Opinion Dynamics, Mathematical Modeling of Epidemic Spreading, Intelligent Transportation System, Federated Learning, Machine Learning with Graphs, Video Analytics, Wireless Sensor Networks, Meta-heuristic Optimization, Internet of Things, Cloud and Fog Computing, Robotics, Information security, Wireless networks, Mobile edge networks, Vehicular Networks, Wireless Edge Computing, Information Security, Biometric Security, Adversarial ML, Cyber Warfare, Application Security, Smart Agriculture, Bibliometrics, Smart Disaster management, Smart Healthcare
2.	Electrical Engineering (EE)	Power & Energy Systems: Power System Dynamics & Control, Stand Alone power System, Power system operation & Optimization, Renewable energy systems (solar, wind and Hybrid), Probabilistic Power System, Uncertainty quantifications, Deregulation, Distributed Generation, Applications of Energy Storage Devices to Power System. Flexible AC Transmission System, Energy System Planning & Auditing, Loss Allocation, Power Systems Stability & Control. Electricity Market, Security Analysis, Fault Detection, Smart Energy Network, Reliability Engineering, Power Systems Analysis and Smart Grid, Failure Analysis, RAMS.

		Power Electronics & Electrical Drives: Power Electronics, Power Quality, Improved Power Quality Convertors, Custom Power Devices (DSTATCOM, DVR & UPQC), Active Power Filters, Multi-level converters/inverters, Matrix converters, Application of Power Electronics to Renewable Energy Systems (Solar, Wind, Fuel Cells etc.), Grid Integration of Renewable Energy Sources, Smart DC Grids (Micro/Nano), Smart AC-DC Grids, Modeling, Design and Digital Control of DC-DC Conversion System, Battery Management System (BMS), Electric Drives, Flexible AC Transmission System, Special Machines & Control, Modulation Techniques, Electric Vehicles, Design of DC fast chargers for EV/LEV, Control of Renewable Energy Systems, Variable frequency transformer. Control & Automation: Control Theory, Model Order Reduction of Linear, Non-Linear and Parametric systems, Finite Element modeling of Distributed Parameter systems, efficient Algorithms for fast simulation of power electronics circuits & power systems, vector control of electric drives, Disease Modelling, Artificial Intelligence and Deep Learning. Cyber-Physical Systems, Learning Control, Networked Control Systems, Decentralized control, Constrained control systems, Mechatronics.
3.	Electronics and Communication Engineering (ECE)	Artificial Intelligence, RF and Microwave Circuits and Devices, Antenna Design Nanoscale semiconductor devices, Electronic Circuits & Devices, Photonics devices, Semiconductor Opto-electronic Devices Communication Systems & Networks, Optical & Wireless Communication Systems, Image Processing, Signal Processing, Machine Learning, Block-chain, Peer-to-Peer Networks, Neural Networks, Game theory, Computer Vision, Robotics, Pattern Recognition, Data Science and Analysis, 3G, 4G, 5G and 6G Technology, Telecommunication Networks, IoT, Digital/Analog VLSI Design, Low Power VLSI Design, VLSI Testing, Analog and Mixed Signal IC Design, VLSI Architecture for Signal/Image Processing, Embedded Systems, Underwater Image Processing, Smart Agriculture, High-Frequency Circuit Design, RF Energy Harvesting, Biosensors, MEMS.

Table II (Full Time)

1	Electronics and Communication Engineering (ECE)	Digital/Analog VLSI Design, Low Power VLSI Design, VLSI Testing, Analog and Mixed Signal IC Design, VLSI Architecture for Signal/Image Processing, Embedded Systems etc.
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B. Eligibility Criteria

An applicant possessing the prescribed qualifications as mentioned below shall be eligible for admission to the PhD programme (under Visvesvaraya scheme). The course as well as University/Institute/Organization of the qualifying degree must be recognized by AICTE/ UGC or equivalent and will be verified by the Institute before registration. The candidate can apply in the following categories:

- PhD programme (Full-Time) under Visvesvaraya scheme (C2S)
- PhD programme (Part-Time) under Visvesvaraya scheme

1. PhD programme (Full-Time) under Visvesvaraya scheme for working professional

Applications are invited for one seat under Visvesvaraya PhD Scheme for Electronics and IT: Phase-II (**Chip to Start-up Programme**) supported by the Ministry of Electronics and Information Technology (MeitY), Govt. of India, in the Electronics and Communication Engineering

The minimum educational qualifications are as follows:

The application must have master's degree in engineering / technology in the relevant area of research with a first class or minimum 60% marks (or CGPA of 6.5 on 10-point scale) in the qualifying examination. The applicant must be GATE/NET qualified.

2. PhD programme (Part-Time) under Visvesvaraya scheme

Applications are invited for Part-Time PhD under Visvesvaraya PhD Scheme for Electronics and IT: Phase-II, supported by the Ministry of Electronics and Information Technology (MeitY), Govt. of India, in the following areas:

- Computer Science and Engineering
- Electrical Engineering
- Electronics and Communication Engineering

2.1 Candidates from organizations that have a Memorandum of Understanding (MoU) with NIT Delhi.

OR

Regular faculty/ Teaching cum Research Fellow or Scientists from Educational Institutions ranked within the top 100 by NIRF, or within the top 1000 QS Ranking, or Institutes of Eminence, or Scientists from Government Funded Research and Development organizations of National Repute/ Government Department/ Government Institute / Government Undertaking or Regular Faculty from Govt. Institutes / Reputed research organization (among Fortune 500 companies).

2.2 The minimum qualification and Experience requirement for Part-Time (Working Professionals) candidates are as follows: -

The applicant must have a master's degree in engineering/technology in the preceding relevant area of research with a first class or minimum 60% marks (or CGPA of 6.5 on a 10-point scale) in the qualifying examination.

OR

Bachelor's degree in engineering/ technology/BS (4 years)/M.Sc. (in relevant discipline)/ MCA with a CGPA of 6.5 and above on a 10-point scale or 60% aggregate from a Centrally Funded Technical Institute (CFTI)/State-funded institute/ university / AICTE Approved Institutes.

2.3 Experience requirement: -

All such candidates should have a minimum experience as given in Table 2 at the level of Assistant Professor or Equivalent as a regular employee in the sponsoring institution/university/college/organization or R&D organization/national laboratory/ industry at the time of applying for admission.

Table 2: Experience Required for Admission to Part-time Ph.D. Programme

S. No.	Qualifications	Work Experience (Post Qualification)
1	M.E./M.Tech. or equivalent /B.E./B.Tech. /M.Sc. or equivalent, from CFTIs/Central Universities	1 Year
2	M.E./M.Tech. or equivalent/B.E./ B.Tech. / M.Sc. or equivalent, from other than CFTIs/Central Universities	2 Years
3	M.E./M.Tech. or equivalent / B.E. / B.Tech./ M.Sc. or equivalent, and working in NIT Delhi (Project or Regular)	1 Year

The applicant is in a position to complete mandatory course requirements along with regular counterparts; the applicant ensures that his/her official duties permit him/her to devote sufficient time for research work (No Objection Certificate required from the employer).

2.4 Part-Time candidates are required to submit a "No Objection Certificate" on a proper letterhead from the appropriate authority in the organization, clearly stating the following:

- The candidate is permitted to pursue studies on a part-time basis and will complete the residential requirement as per NIT Delhi norms.
- That his/her official duties permit him/her to devote sufficient time to research work.
- Facilities for research in the candidate's field of research are available at the candidate's place of work.

3. Financial Support for Full-Time PhD candidates under Visvesvaraya PhD Scheme:

Fellowship: Rs. 46,250/- per month in 1st & 2nd Year and Rs. 52,500/- per month in 3rd, 4th and 5th years of PhD (support till PhD completion or 5 years, whichever is earlier), as per Visvesvaraya PhD Scheme for Electronics and IT: Phase-II.

Research Contingency Grant Support: An amount of Rs. 1.20 Lakh per year for a Full-Time PhD candidate, released to the institution on a pro-rata basis after completion of the required tenure by the candidate, as per Visvesvaraya PhD Scheme guidelines.

Reimbursement of Rent (RoR): Linked with the Fellowship, RoR is admissible @24% of the fellowship for X class city/town, as per the classification notified by the Ministry of Finance for HRA.

Support for Attending International Conference: Available from the 3rd year of PhD, one-time support up to Rs. 1.50 Lakh is provided to a Full-Time PhD candidate for attending an approved International conference where an oral (non-poster) paper has been accepted for presentation, subject to the guidelines of the scheme. The grant is released to the institution, not to the applicant.

Visit to Labs Abroad: A one-time provision, available to selected Full-Time PhD candidates from the 3rd year, for a 6-month visit to a lab abroad. During the visit, the Monthly Fellowship is Rs. 1.50 Lakh, with Travel/Visa expenses up to Rs. 1.50 Lakh; domestic Fellowship and RoR remain suspended during this period. The visit must be completed at least 3 months before completion of PhD or the 5-year support period, whichever is earlier, subject to selection by the Academic Committee.

4. Financial Support for Part-Time PhD candidates under Visvesvaraya PhD Scheme:

Fellowship/Incentive: The Part-Time PhD candidate who does not avail of any PhD Fellowship/scholarship/stipend from any Central or State Government scheme is eligible to receive a one-time incentive of Rs. 3.00 Lakh, through DBT to his/her bank account, on successful completion of the PhD degree and the requisite formalities with the institution and the scheme (submission of completion documents, claims, etc., through the institution to Digital India Corporation, DIC), as per Visvesvaraya PhD Scheme guidelines.

C. Important Points

- PhD course work for Executives (having work experience of 15 years or more in Government/ State Government/ Autonomous Bodies/ Reputed Companies) may be conducted through Project Work/Seminar.
- Merely fulfilling the above minimum eligibility criteria does not ensure the selection or even eligibility for the entrance test/interview or both.
- Regarding selection, the decision of the Competent Authority will be treated as final and binding.
- **Any request will not be entertained to conduct online presentations and interviews.**

D. PhD Seats:

Visvesvaraya PhD Scheme for Electronics and IT: Phase-II	
Full-Time	Part-Time
01*	01**

*Full-Time seat allotted under the Chip to Start-up (C2S) Programme, vide Implementation Order No. PhD-06/09-2026/89 dated 31 Aug 2026, Digital India Corporation, MeitY. **Part-Time seat allotted under the Visvesvaraya PhD Scheme for Electronics and IT: Phase-II, vide Implementation Order No. PhD_PT-03/08-2026/49 dated 31 Aug 2026

E. Admission Procedure

- i. The admission shall be solely based upon the performance of eligible candidates during “presentation and interview” and availability of the Supervisor in the proposed area of research.
- ii. The admission to PhD programme, Full-Time (ECE only) and Part-Time (CSE, EE and ECE), in this advertisement shall be made purely on the basis of a “**presentation**” to be made by the eligible applicants in their proposed area of research followed by “**interview**” to be conducted by the respective Department Admission Committee.
- iii. The Department Admission Committee for each Department shall recommend suitable candidates for admission based on its assessment of presentation and interview.
- iv. The Department Admission Committee/Institute reserves the right not to recommend any candidate for admission to PhD in the respective department if the performance of the candidates is not found satisfactory during “presentation and interview”. The decision of the Competent Authority shall be final and binding.

All the applied/ selected candidates will be governed by the rules and regulations of the PhD Ordinance available at the Institute website. Candidates are advised to read the ordinance clearly before proceeding with the application.

F. Procedure for Online Application/ How to Apply:

1. Students must fill up the online Google Form within the stipulated time mentioned. Late submissions will not be entertained, under any circumstances. One application format is attached here, which needs to be filled with the desired documents in JPG/ PDF and upload on Google form.
2. The candidate must fill up the online Form within the stipulated time mentioned. Late submissions will not be entertained, under any circumstances.
3. Link for the Online Form: <https://forms.gle/puxYgfGasXXvHP688>
4. **While filling out the online Google Form, candidates are advised to read up all the requisite details very carefully, correctly and upload all the relevant documents along with application form (available at the end of this advertisement) in PDF format. The same application form is available in doc format with the advertisement on the institute website also.**
5. Candidates are advised to visit the institute website: www.nitdelhi.ac.in for frequent

updates.

6. A candidate interested in applying in more than one category shall be required to submit separate application forms along with requisite fees and documents.
7. Application Fee (**non-refundable**):
 - (a)UR/ OBC-NCL/ EWS Candidates: Rs. 1000/-
 - (b)SC/ ST/ PwD Candidates: Rs. 500/-
8. The application fee can be paid online through the following link:
<https://rzp.io/rzp/zEHo3eo>
9. Candidates should mention **Transaction ID after paying the fee** in appropriate section of the application form. The application fee is **non-refundable**.
10. Part-Time candidates should produce a No Objection Certificate (NOC) from their Employer at the time of Interview/Document Verification.
11. **Document verification:** the certificates (in the original) of all the candidates recommended for admission shall be checked by the individual department.
12. Incomplete, improper, and wrong applications will not be considered for presentations and interview.
13. Any legal dispute pertaining to admissions shall fall within the jurisdiction of High Court of Delhi only.
14. All admissions will be provisional till these are confirmed subject to medical fitness, payment of all the fees, fulfillment of eligibility conditions, and verification of certificates by the Office of Dean R&C of the Institute.
15. After payment of admission fee, no fee refund shall be entertained in any case of surrender of seat except caution money.

I. Important Dates

Sr. No.	Activity	Date
1.	Last date for submission of Applications (Strictly through Online Mode)	October 8 th , 2026
2.	Display of list of candidates eligible for presentation & interview	October 9 th , 2026 (AN)
3.	Presentation/ interview by all eligible candidates Through offline (Physical) mode only.	October 13 th , 2026 (AN onwards)
4.	Display of list of selected candidates	October 13 th , 2026
5.	Deposition of fee (Online)	14 th - 18 th October, 2026
6.	Physical reporting in the Institute/ Document Verification	October 26 th , 2026
7.	Commencement of Classes	As per academic calendar of NIT Delhi

Note: The institute reserves the right to fix suitable criteria for short listing of eligible candidates satisfying qualification and experience. The Institute reserves the right to modify or cancel this advertisement/any part of this advertisement at any stage. Only shortlisted candidates may be called for an interview. *Only Caution Money/Security shall be refunded in case a candidate withdraws after taking the admission to the PhD programme.* No request for a refund of any other fee paid shall be entertained under any circumstances.

For any queries, please contact:

Office of Dean (Research & Consultancy),
National Institute of Technology Delhi
Email: phdadmission@nitdelhi.ac.in
Phone: 011-33861112

Dean Research & Consultancy
NIT Delhi
011-33861112
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Application Form for Admission to PhD Program for Academic Year 2026-27 (Autumn Semester)

1. Select program (Anyone)

Ensure your eligibility before selecting the program. If you wish to apply for more than 1 program, please apply separately.

Sl. No.	Department/Discipline	Tick (✓) in the Department/ Discipline you wish to apply
1	Computer Science and Engineering (CSE)	
2	Electrical Engineering (EE)	
3	Electronics and Communication Engineering (ECE)	

2. Select Category of Registration of PhD applied and Details of NET/ NET-JRF/ GATE

Sl. No.	Category of PhD Registration Applied	Tick (✓) in the PhD Registration Applied you wish to apply	Whether NET/NET- JRF/GATE Qualified. If yes, mention Year, Score and Rank	Paste a recent passport size photograph
1	Full Time under Visvesvaraya scheme			
2	Part Time under Visvesvaraya scheme			

3. Name in Full (in BLOCK Letters):

4. Father's Name (in BLOCK Letters):

5. Mother's Name (in BLOCK Letters):

6. Date of Birth (DD/MM/YYYY format):

7. Gender: Male/Female/Others:

8. Address (in BLOCK Letters):.....

District:..... State:.....

Mobile No.:.....E-mail:@.....

9. Aadhar No.:.....

10. Nationality:..... Religion:.....

11. Category (UR/EWS/OBC-NCL/SC/ST/PwD/Foreign Nationals):.....

12. Application Fee paid: Rs./USD (attach transaction slip)

13. Working Experience (in years, if applicable):

.....

14. Working Organization/ Institute Name & Address:

.....

15. Details of Examination Passed (from Class X onwards):

Sl. No.	Examination Passed (Fill the applicable only)	Subject/ Discipline/ Specialization	Year of Passing	CGPA/ Percentage of Marks Obtained	Division (I/II/III)	Name of the Institute &University	Whether Documentary Evidence attached (Yes/No)
1	Secondary School Certificate (Class X)						
2	Higher Secondary School Certificate (Class XII)						
3	Diploma in Engineering/BSc (Tick anyone only)						
4	BE/B.Tech./ M.Sc. (Tick anyone only)						
5	ME/MTech/MPhil (Tick anyone only)						
6							
7							

16. Have you ever been disqualified or debarred from appearing in any examination conducted by a University/Board?

Yes/No

If yes, give details:.....

DECLARATION BY THE CANDIDATE

I clearly understand that my application to the PhD program to the National Institute of Technology Delhi is subjected to the applicable regulations from time to time. I also understand that admission is allowed to me based on the certificates and documents produced by me. I also undertake that the certificates and documents produced by me in original are correct, genuine, and true to the best of my knowledge. If any information/ documents/ particulars are found to be forged or false at any stage in future, then the NIT Delhi can cancel my admission and all the fees deposited by me shall be forfeited. If any degree is awarded based on my admission on the basis on my forged documents, then that certificate/degree awarded by NIT Delhi shall be automatically cancelled. In such case, I shall have no claim, whatsoever, in respect of my admission.

Date.....

Signature of the Applicant